

## PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2002-366534

(43)Date of publication of application : 20.12.2002

(51)Int.Cl.

G06F 15/177

G06F 9/54

G06F 12/06

G06F 15/16

G06F 15/173

(21)Application number : 2002-079360

(71)Applicant : SONY COMPUTER ENTERTAINMENT INC

(22)Date of filing : 20.03.2002

(72)Inventor : SUZUOKI MASAKAZU  
YAMAZAKI TAKESHI

(30)Priority

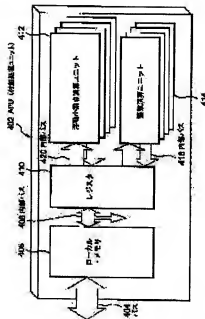
Priority number : 2001 816752 ???Priority date : 22.03.2001 ???Priority country : US

## (54) COMPUTER PROCESSOR AND PROCESSING DEVICE

## (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a computer architecture and programming model for high speed processing through broadband networks.

**SOLUTION:** The architecture employs a uniform modular structure, a common computing module and uniform software cells. The common computing module includes a controller, a plurality of processing units, a plurality of local memories from which the processing units process programs, a direct memory access controller and a shared main memory. A synchronized system and method for the coordinated reading and writing of data to and from the shared main memory by the processing units are provided.



## LEGAL STATUS

[Date of request for examination] 20.08.2002

[Date of sending the examiner's decision of rejection] 06.01.2004

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number] 3696563

[Date of registration] 08.07.2005

[Number of appeal against examiner's decision of rejection] 2004-02181

[Date of requesting appeal against examiner's  
decision of rejection]

05.02.2004

[Date of extinction of right]